## Notice of References Cited Application/Control No. 09/679,456 Examiner Jean M. Corrielus Applicant(s)/Patent Under Reexamination RAGO, STEPHEN A. Page 1 of 1

## **U.S. PATENT DOCUMENTS**

*	:	Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
*	Α	US-5,768,532	06-1998	Megerian, Mark Gregory	709/245
	В	US-			
	C	US-			
	D	US-			
	ш	US-			
	F	US-			
	G	US-			
	Н	US-			
	_	US-			
	J	US-			
	K	US-			
T	L	US-			
	М	US-			

## **FOREIGN PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	0					
	Р					
	Q					
	R					
	S				·	
	Т					

## **NON-PATENT DOCUMENTS**

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Andras et al., "An XML based framework for self-describing parallel I/O data", IEEE, 2003
	<b>v</b>	HDF5, User's guide: Data model, file structure", Chapter 1.
	w	
	x	

\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)

Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.